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S/N 10/552301

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TSUKADA	Examiner:	BAISA, JOSELITO SASIS
Serial No.:	10/552301	Group Art Unit:	2832
Filed:	October 7, 2005	Docket No.:	10921.0359USWO
Title:	CHIP RESISTOR AND METHOD FOR MANUFACTURING SAME		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on May 12, 2008.

By: 

Name: Antonette C. Peters

Mail Stop: RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT

Dear Commissioner:

In response to the final Office Action mailed December 12, 2007, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.